

FIG. 1

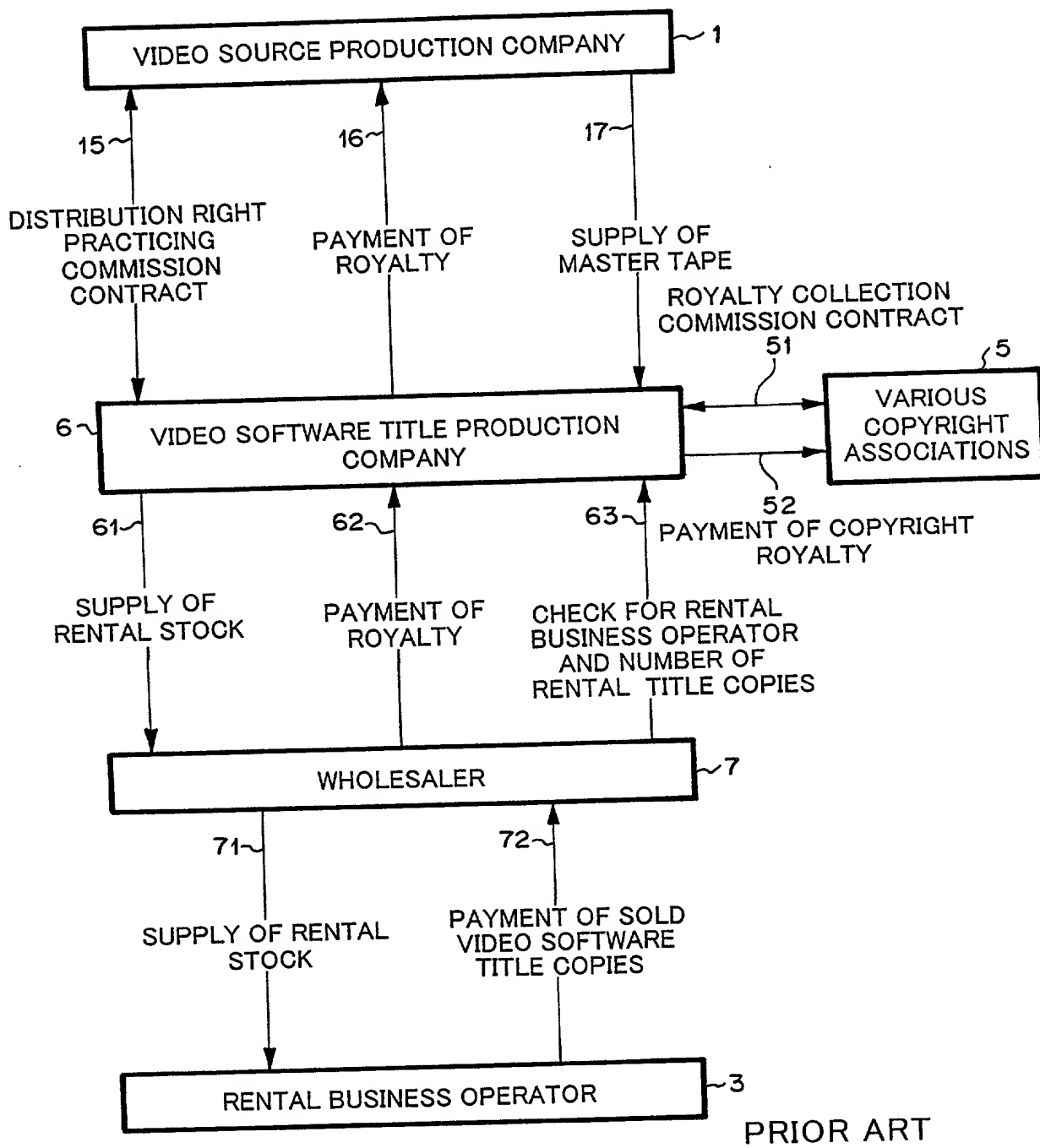
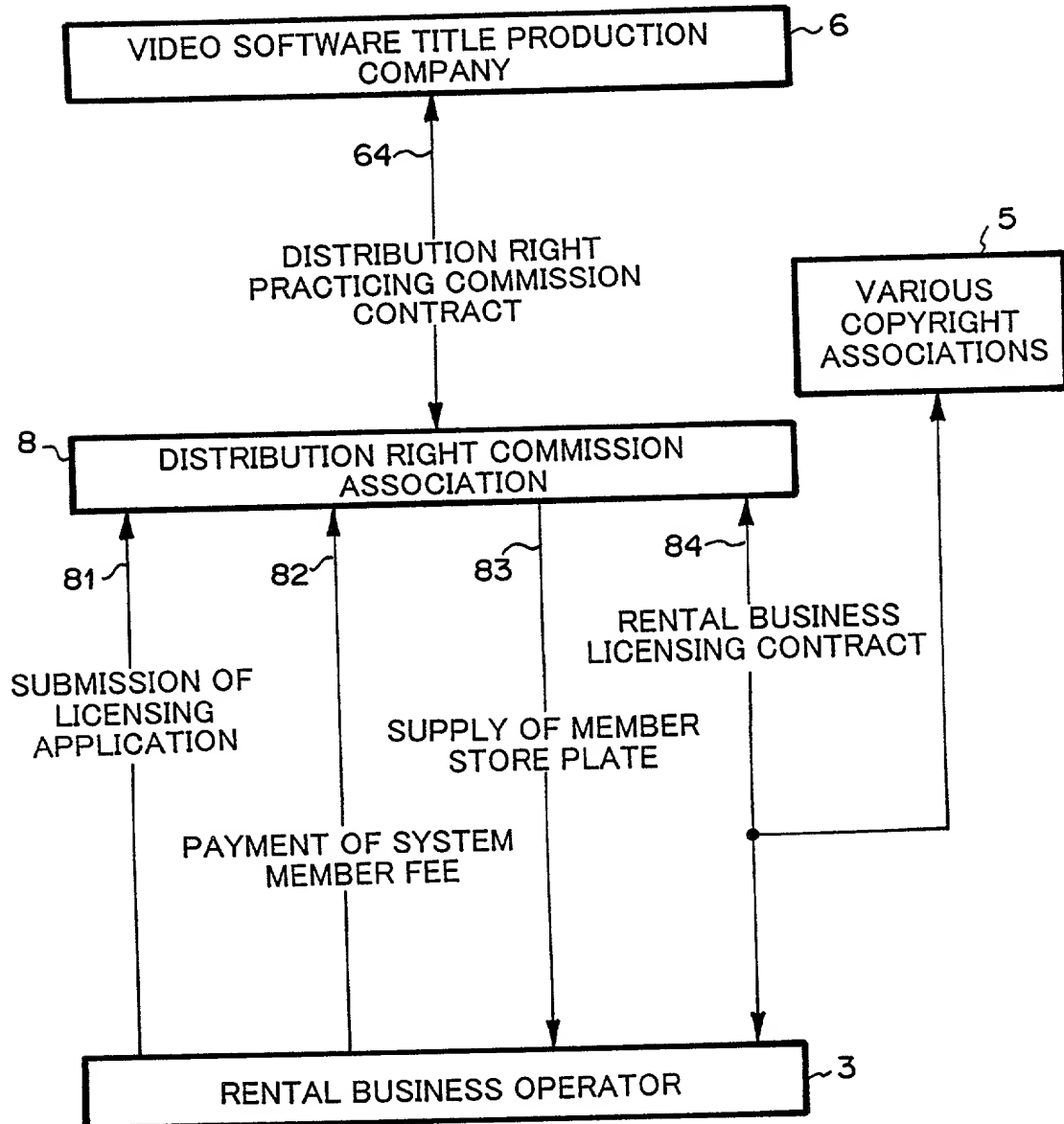


FIG.2



PRIOR ART

FIG.3

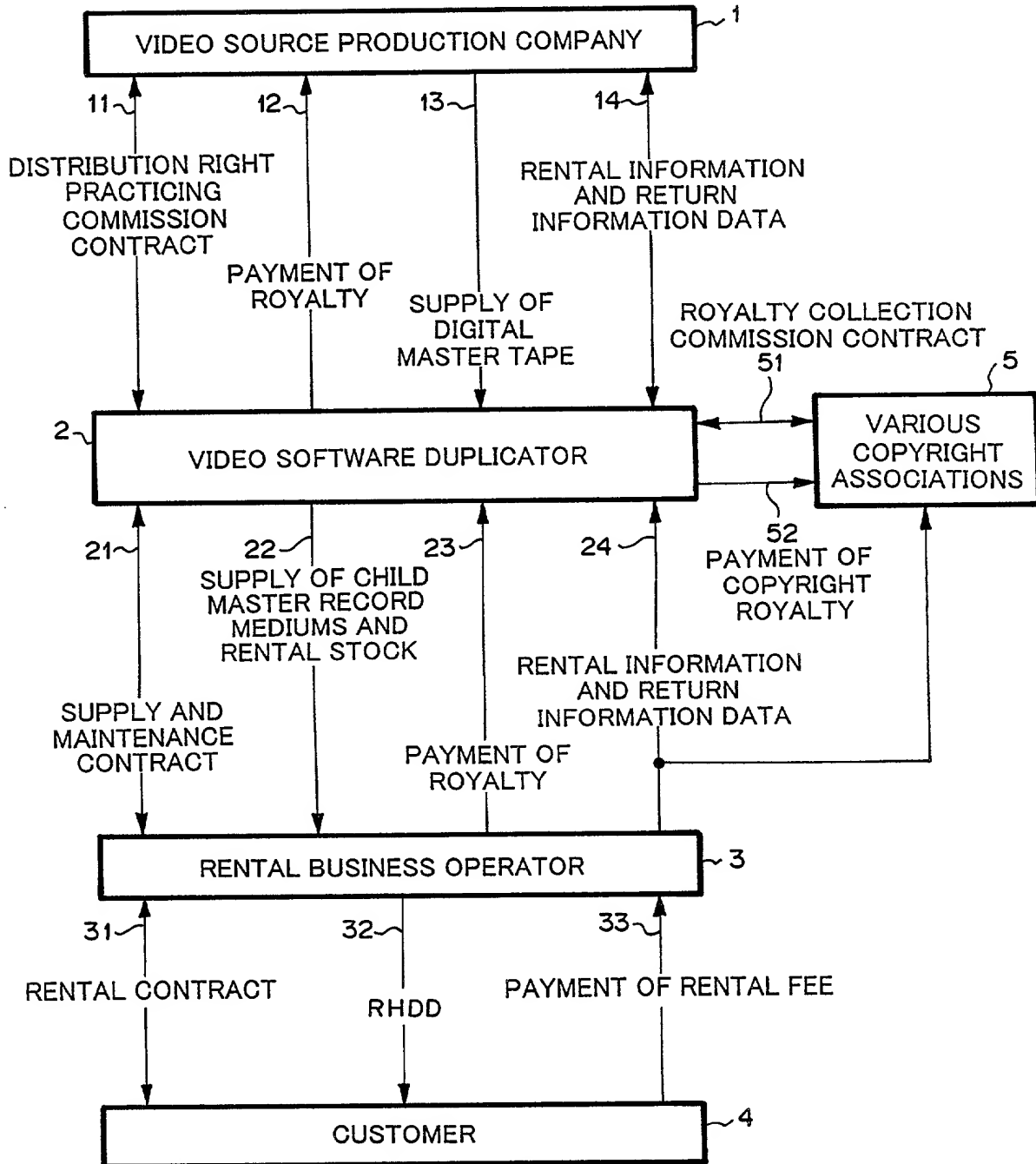
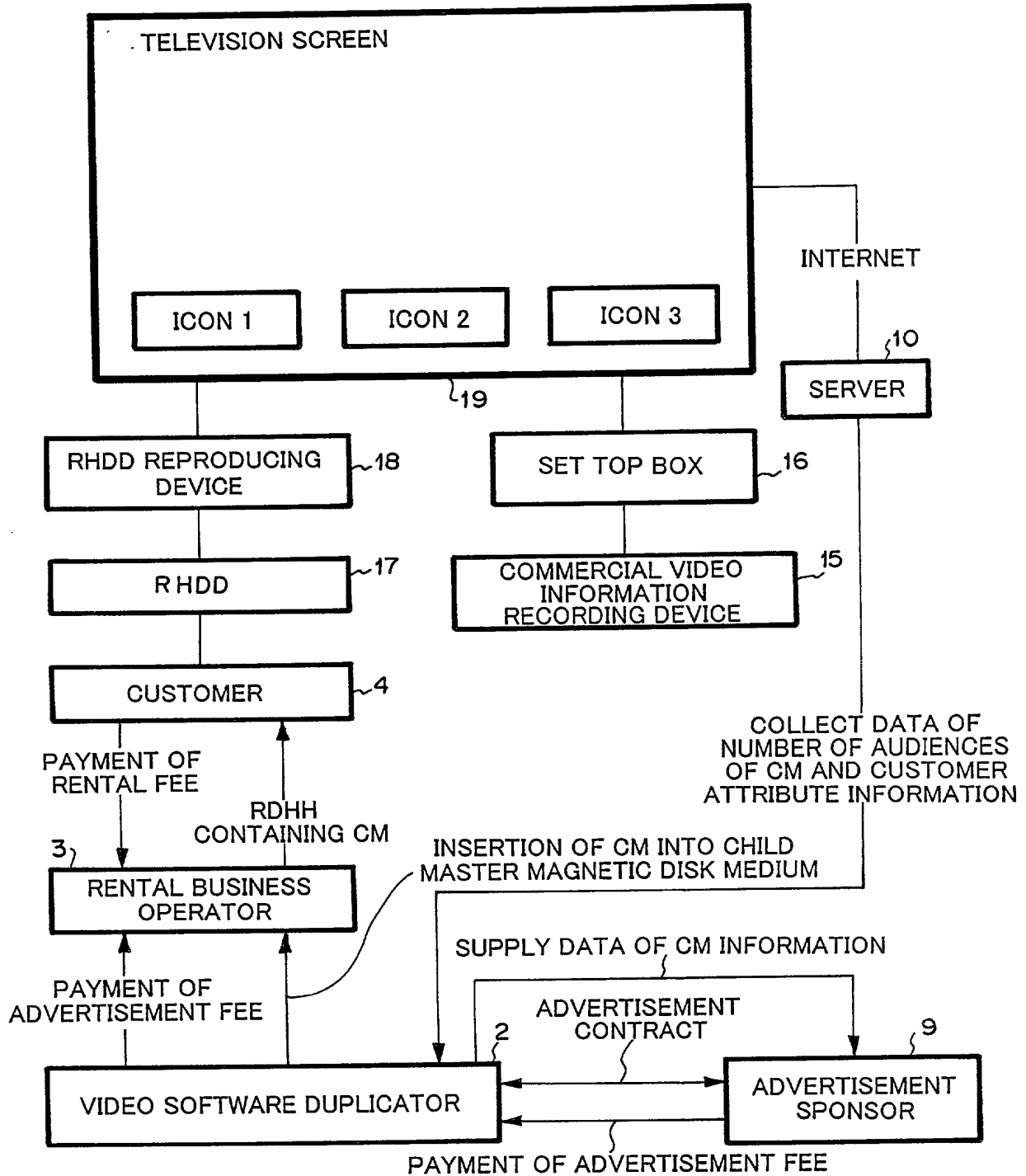


FIG.4



SCANNED # 8

FIG. 5

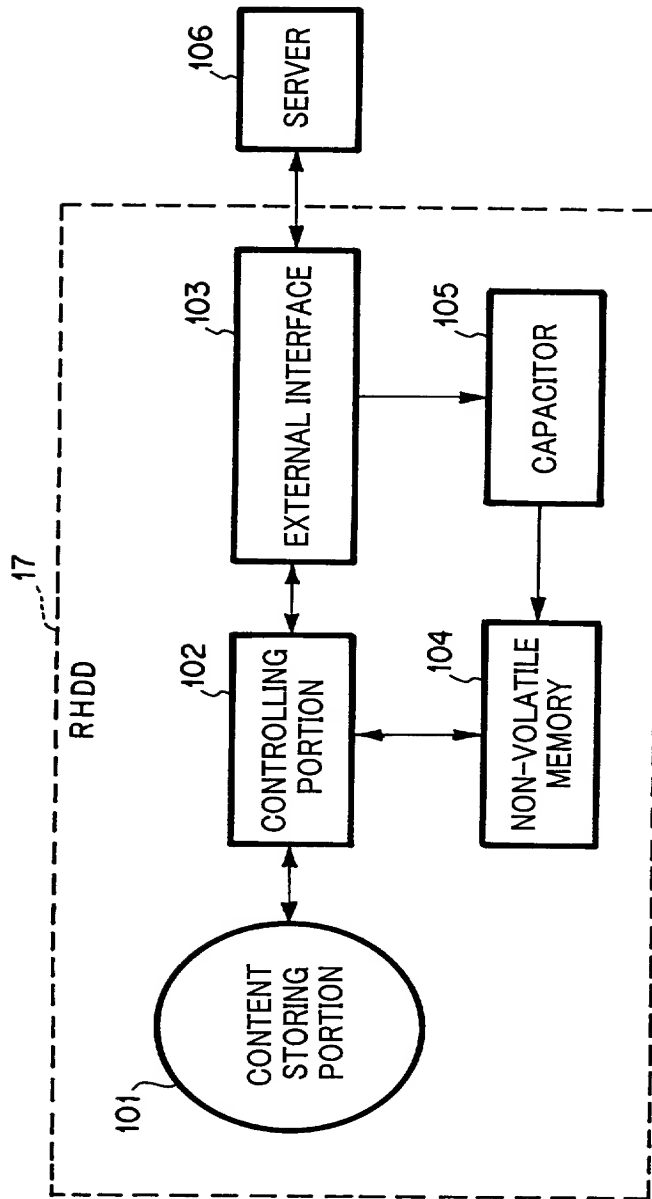


FIG.6

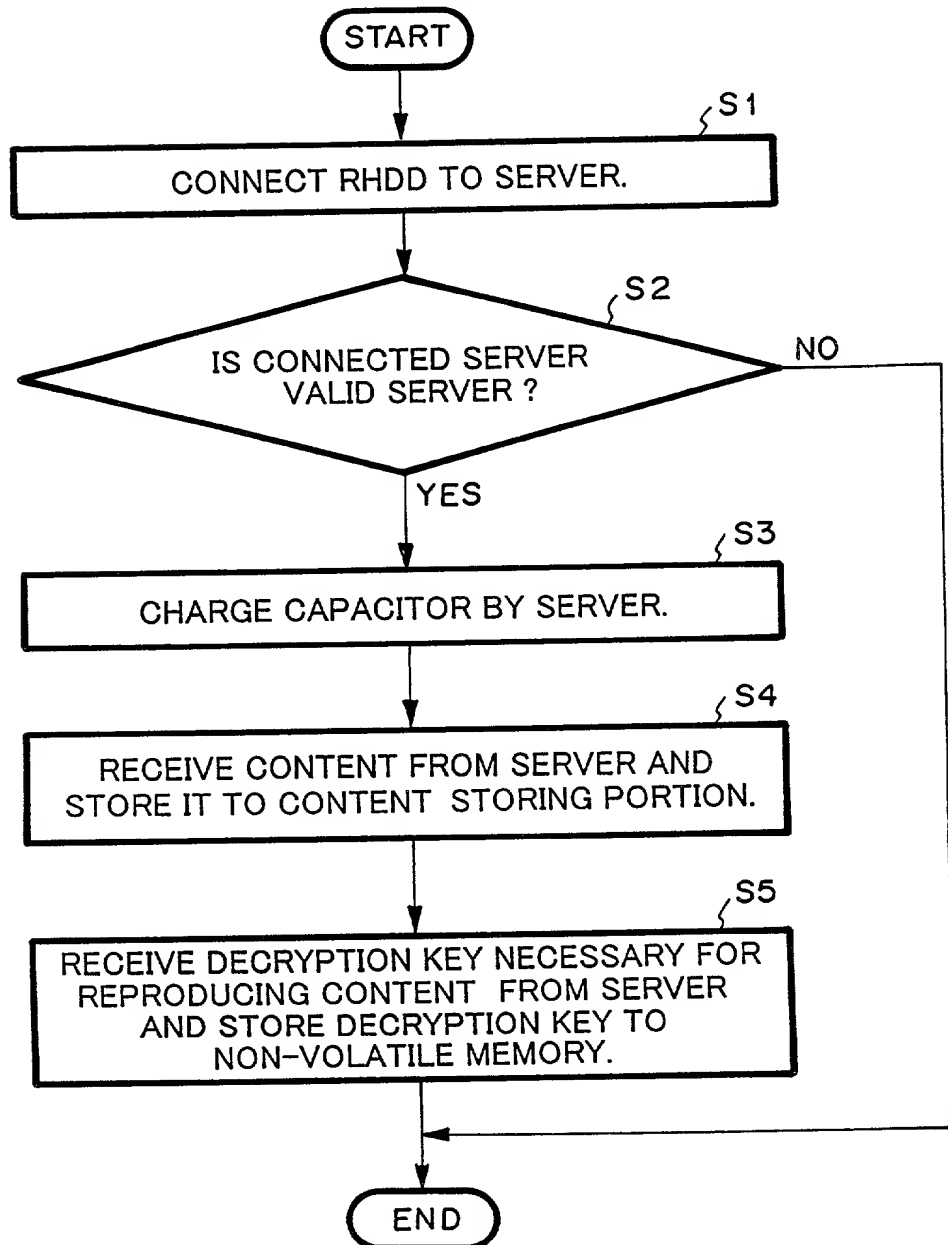


FIG. 7

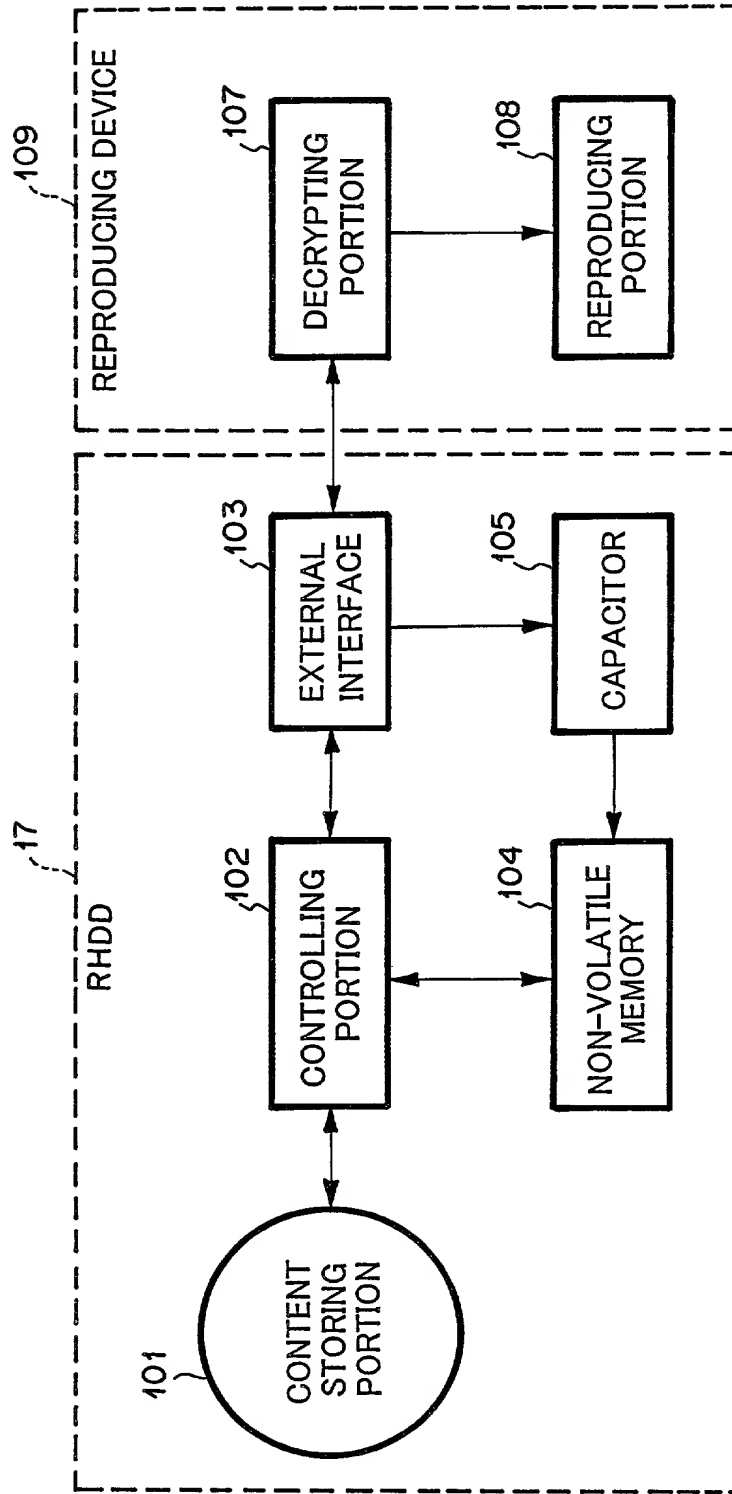
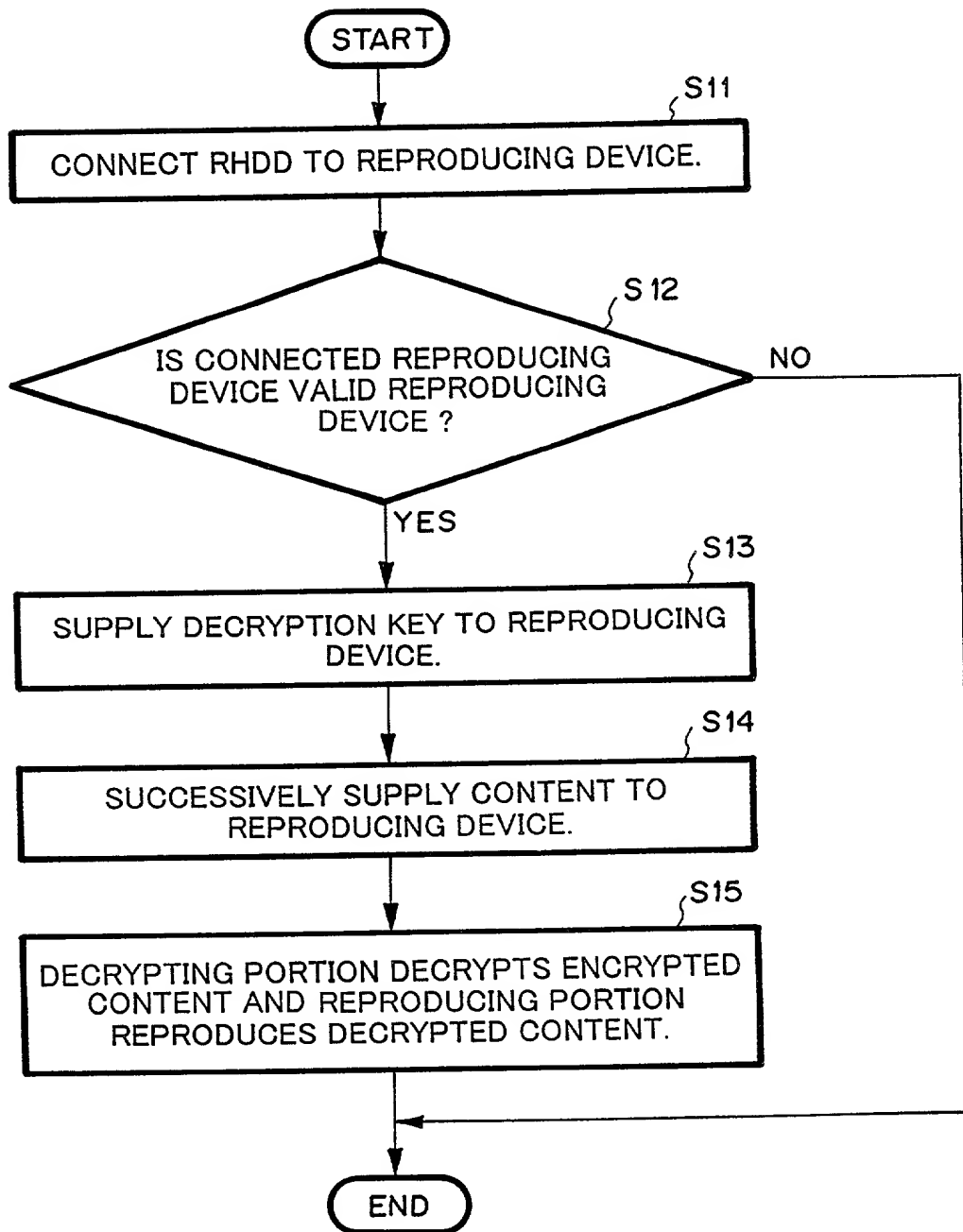


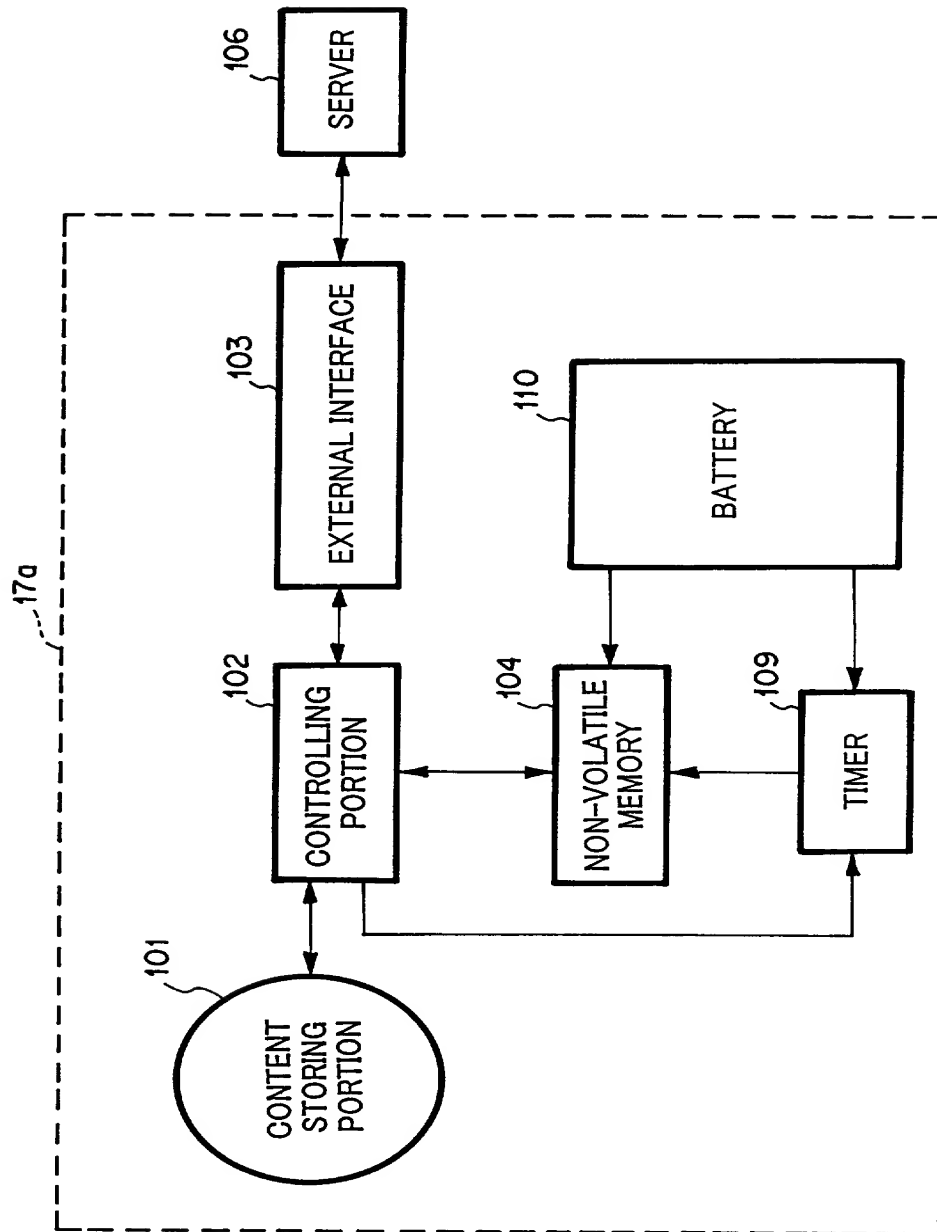
FIG.8





SCANNED, # \_\_\_\_\_

FIG. 9



## FIG.10

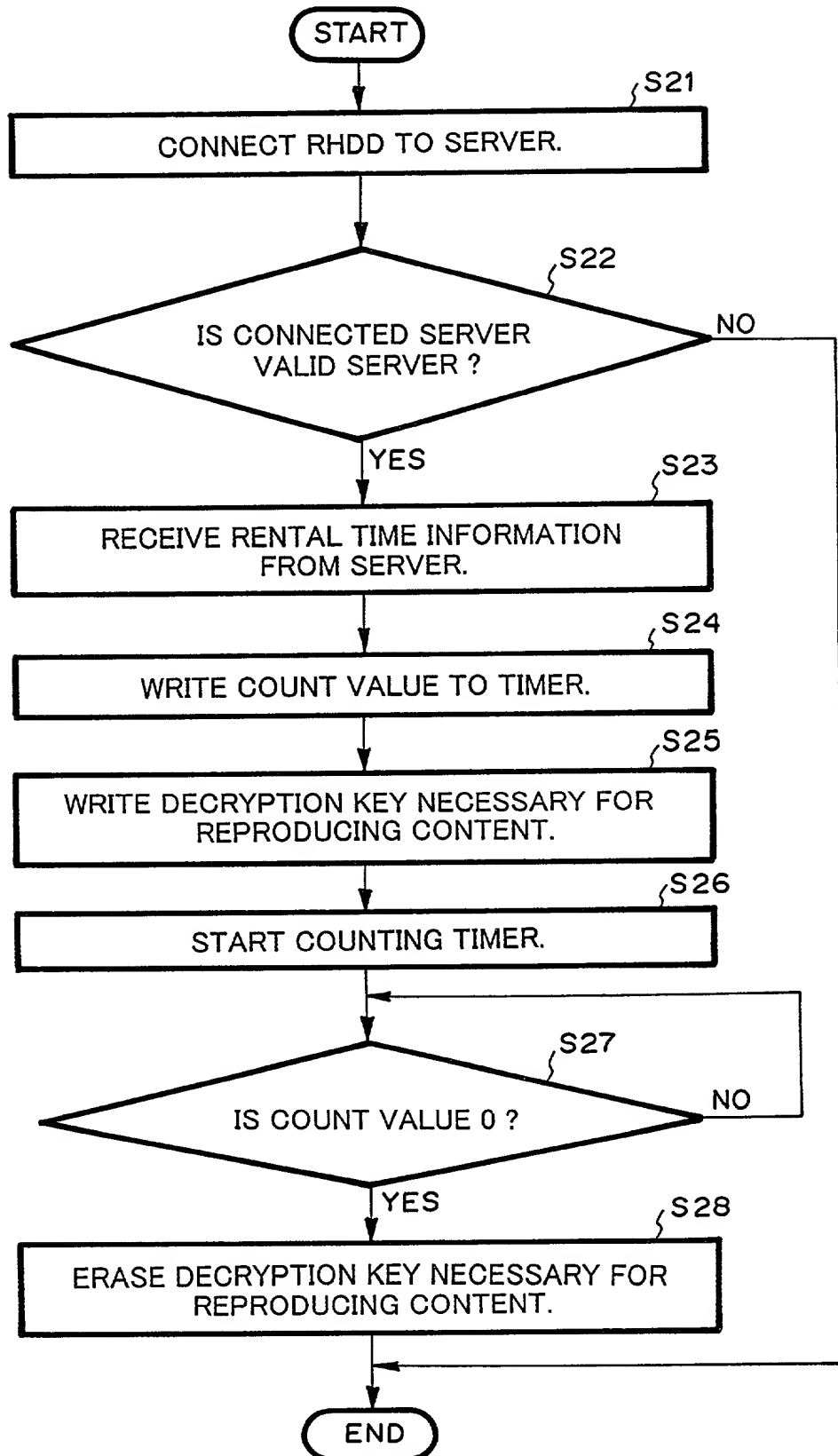


FIG.11

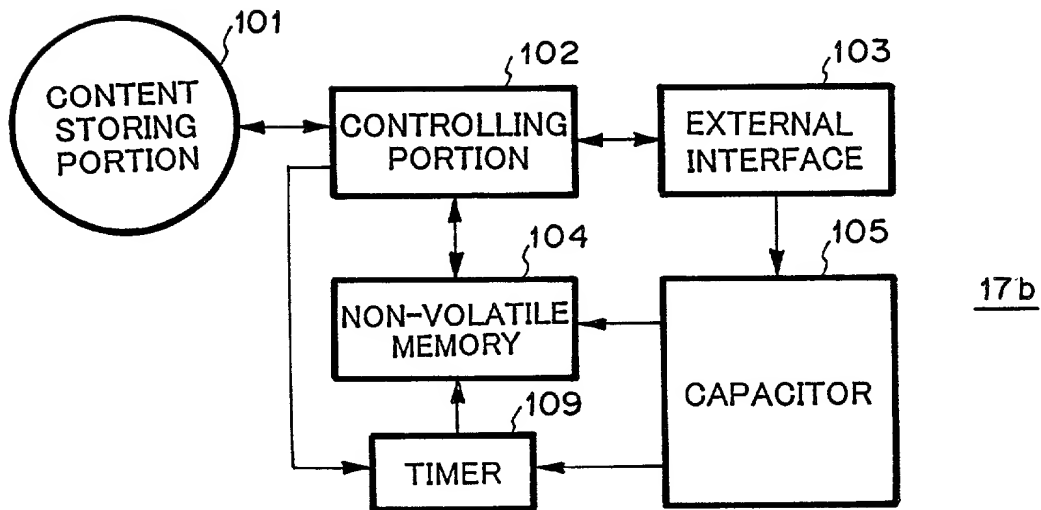


FIG.12

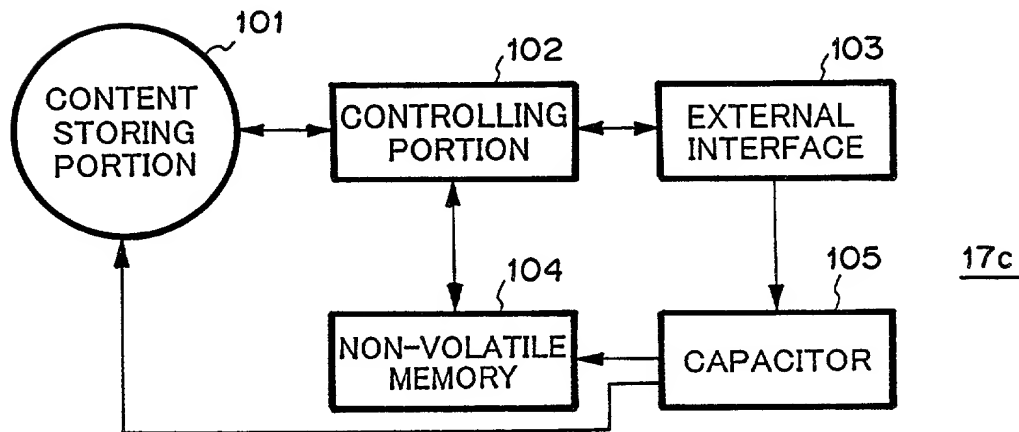


FIG.13

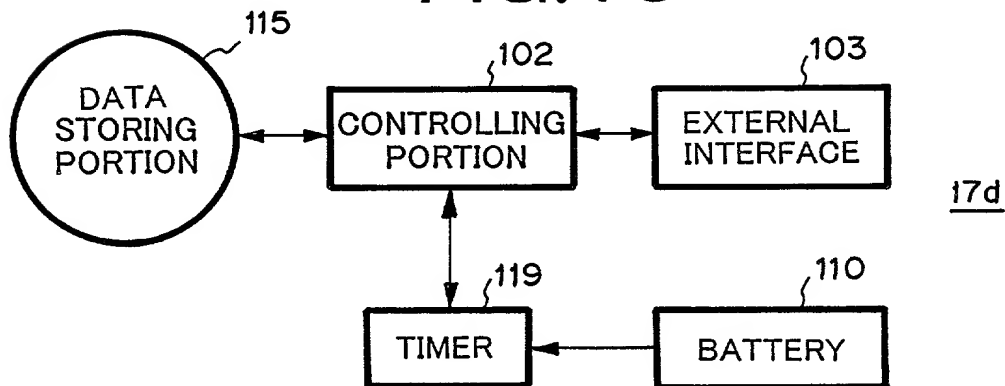


FIG. 14

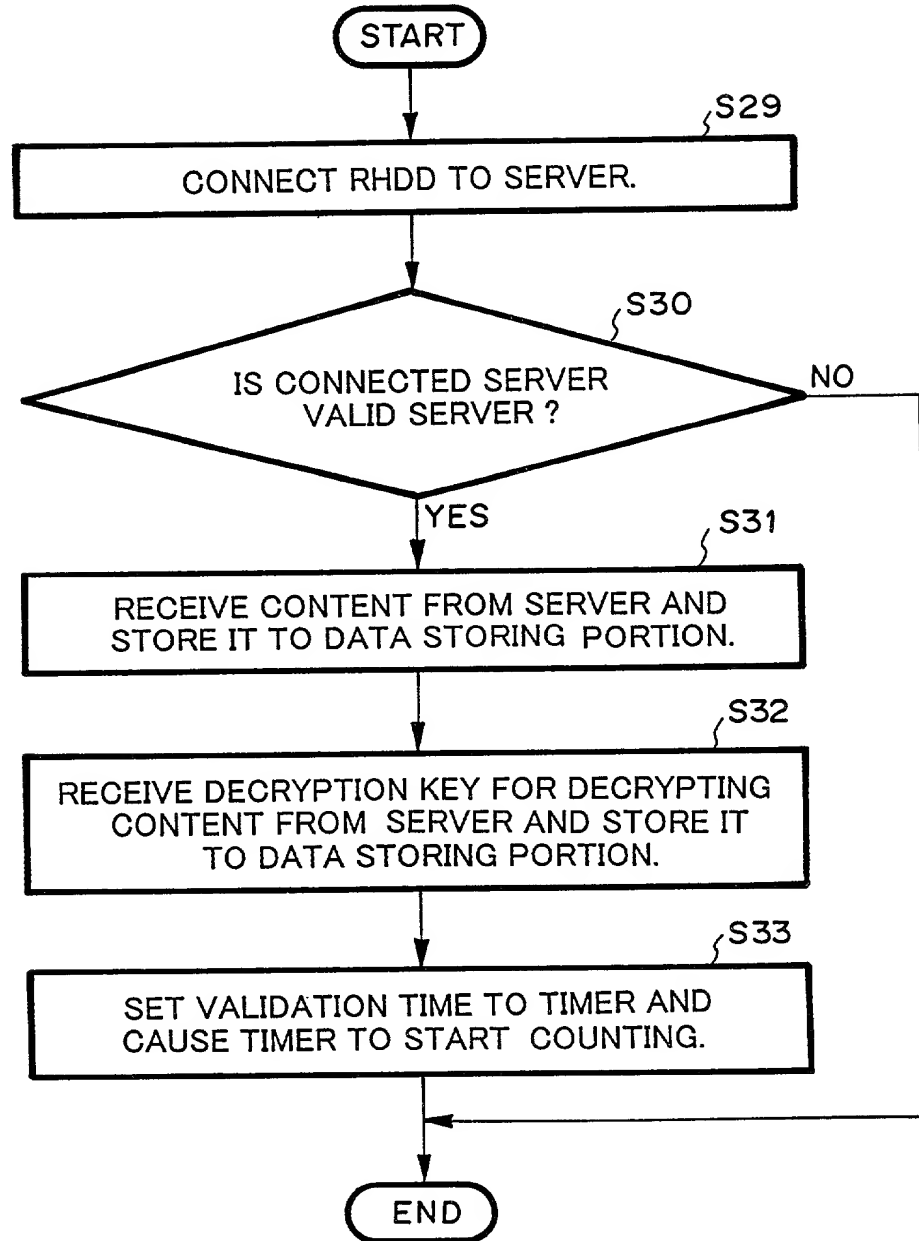


FIG.15

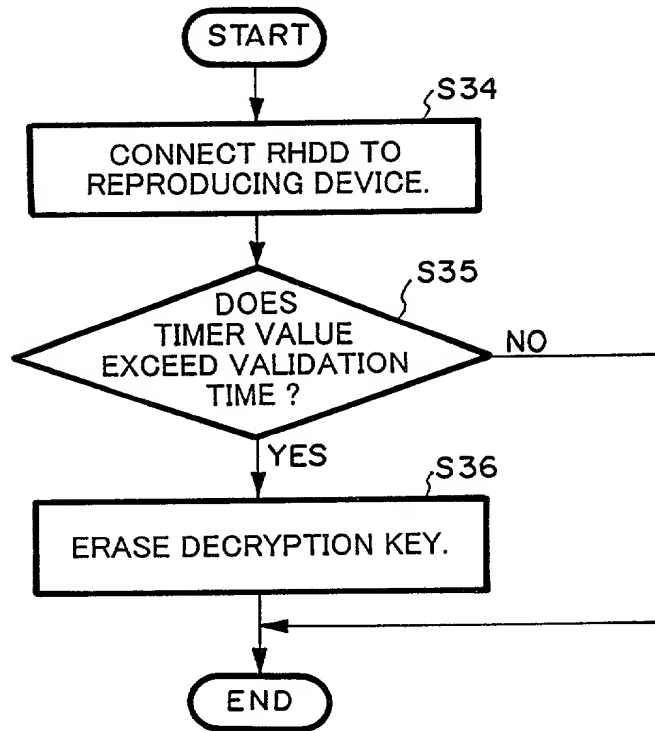


FIG.16

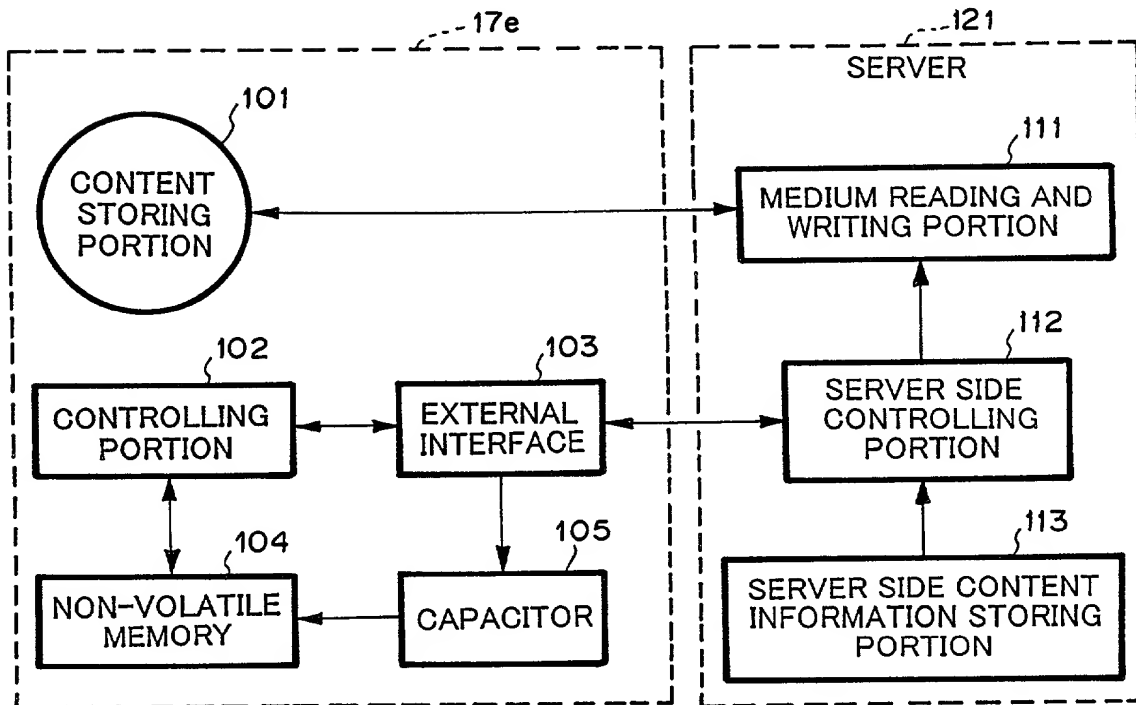


FIG. 17

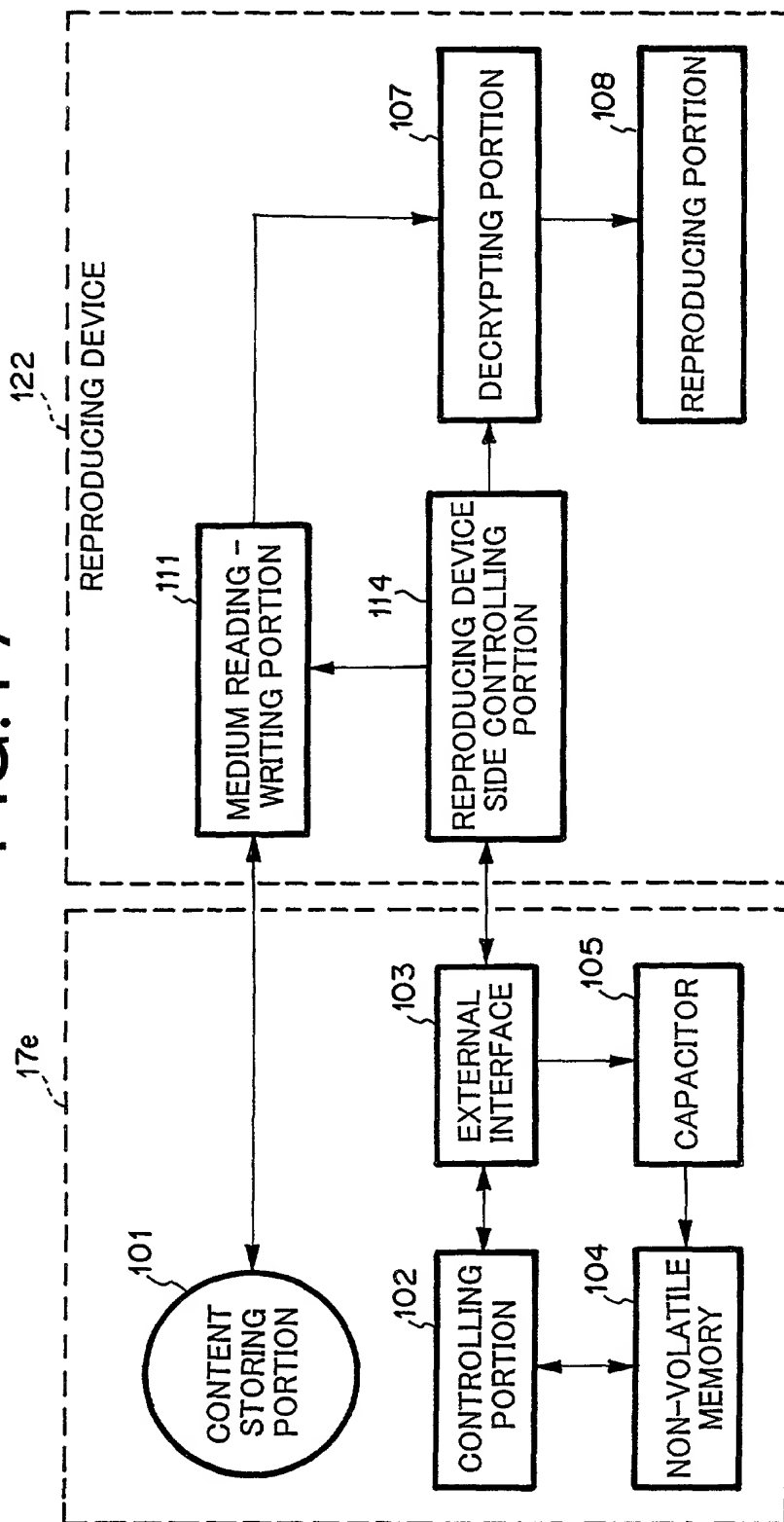


FIG. 18

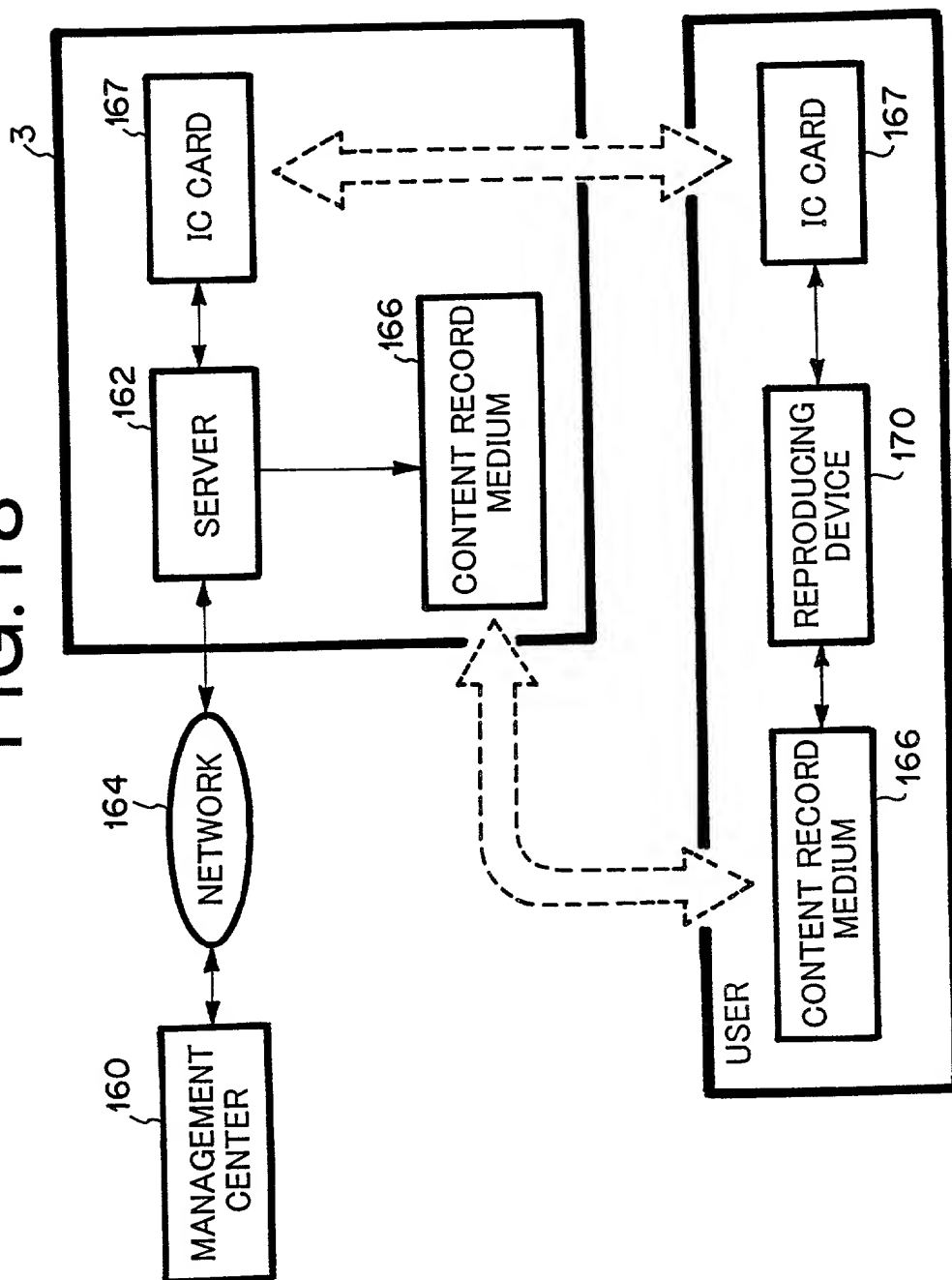
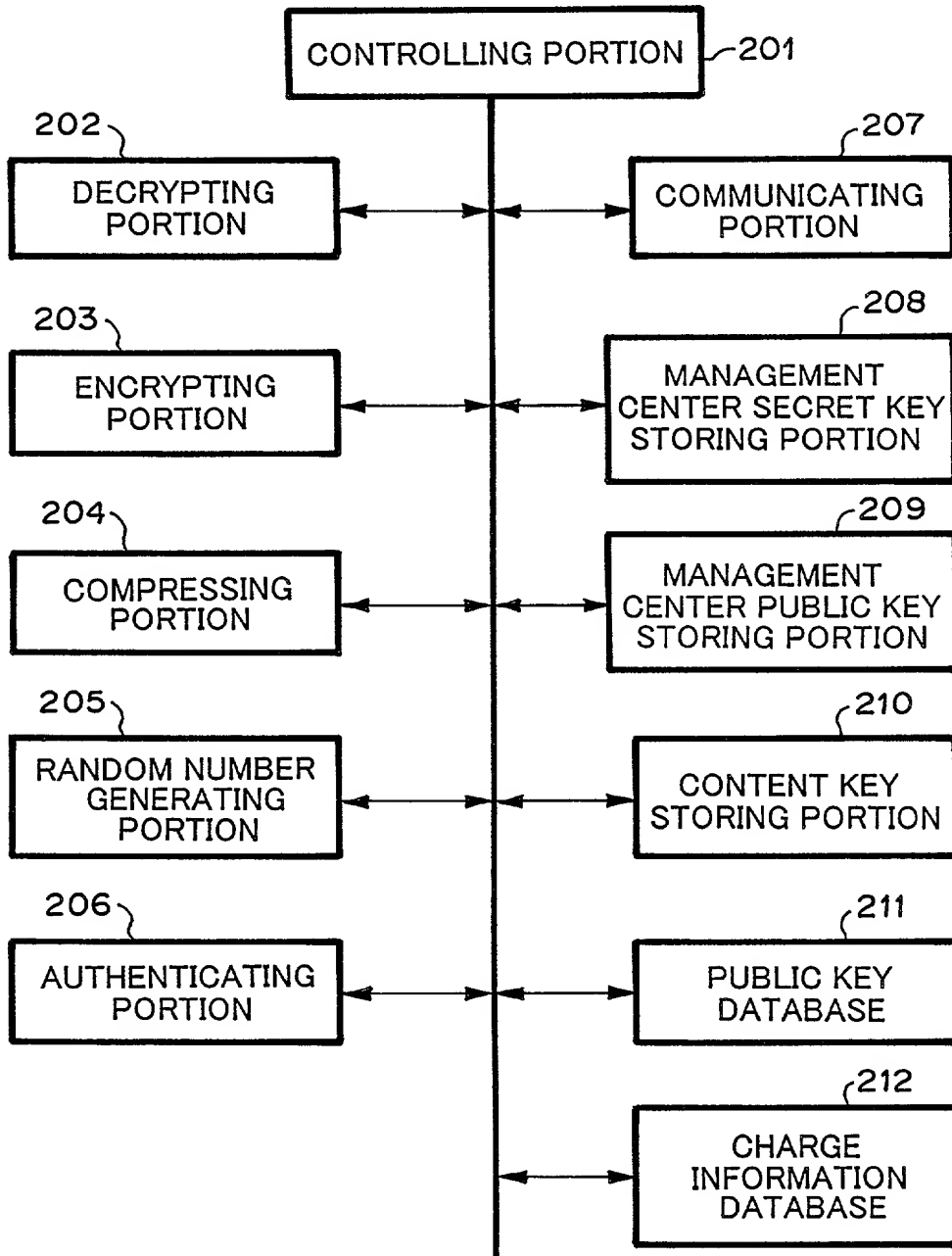


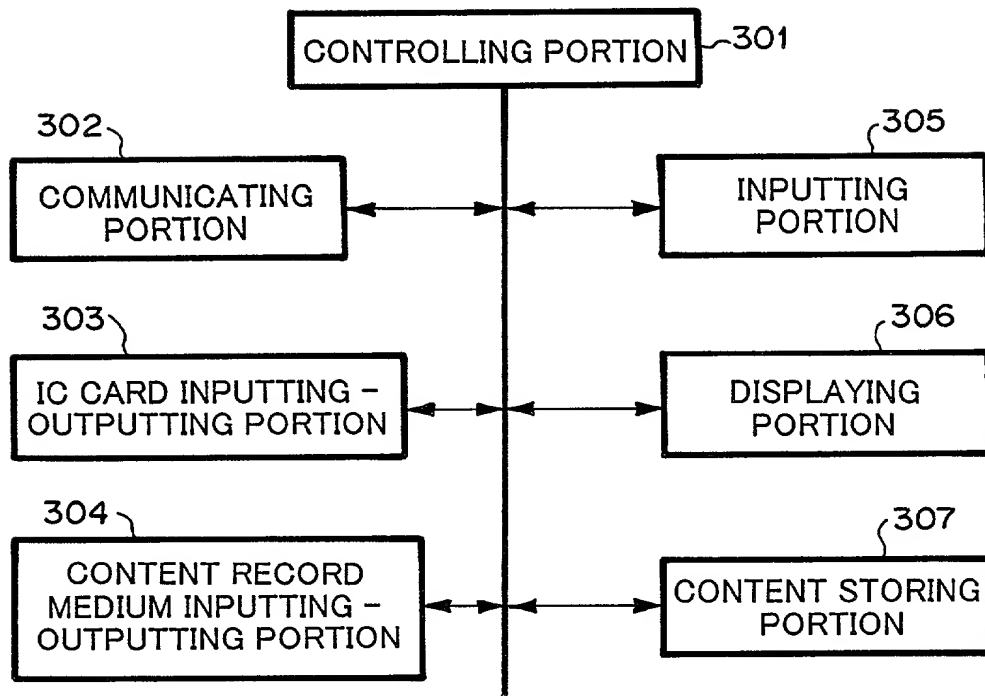
FIG.19



STRUCTURE OF MANAGEMENT CENTER 160

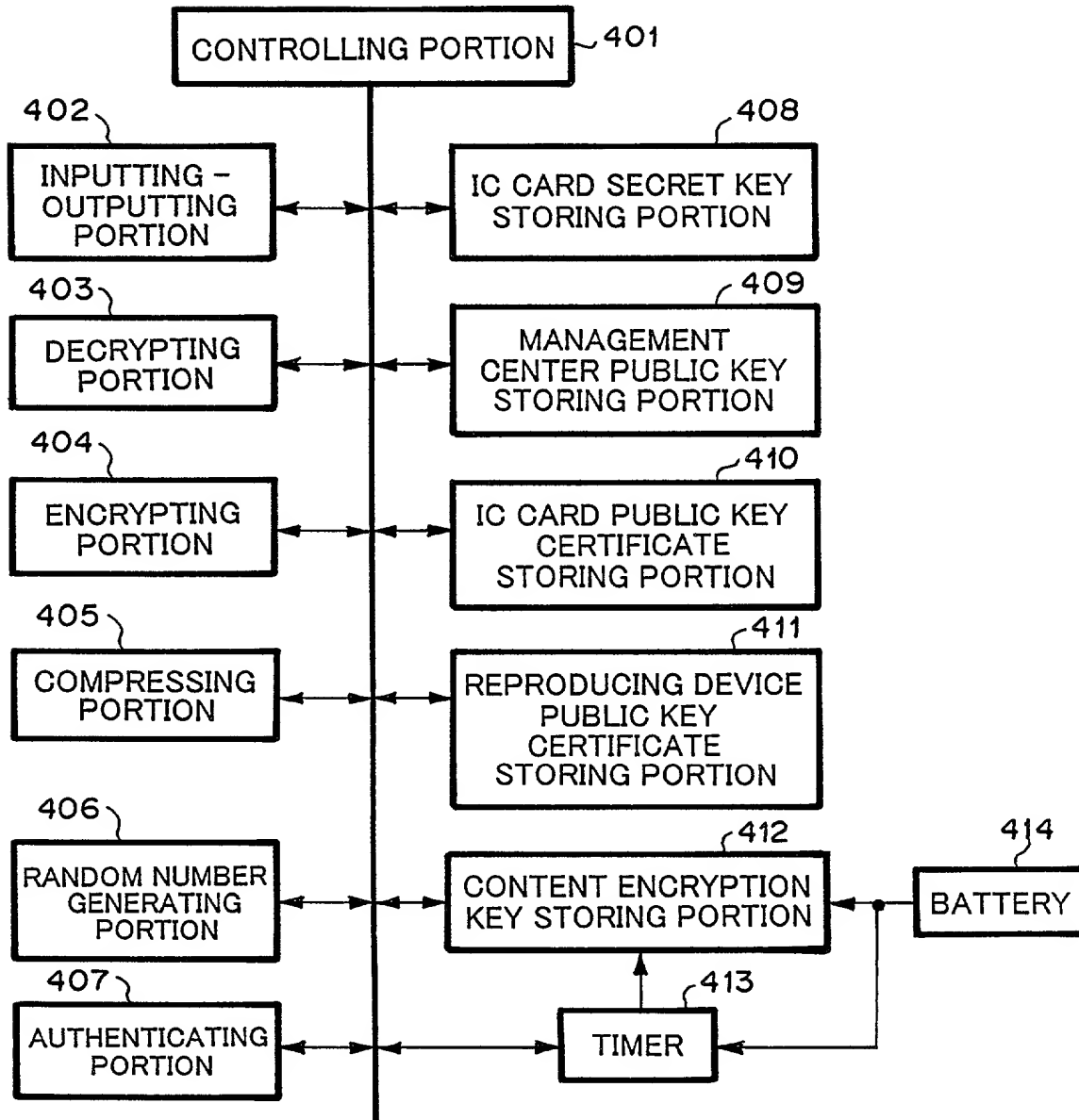


FIG.20



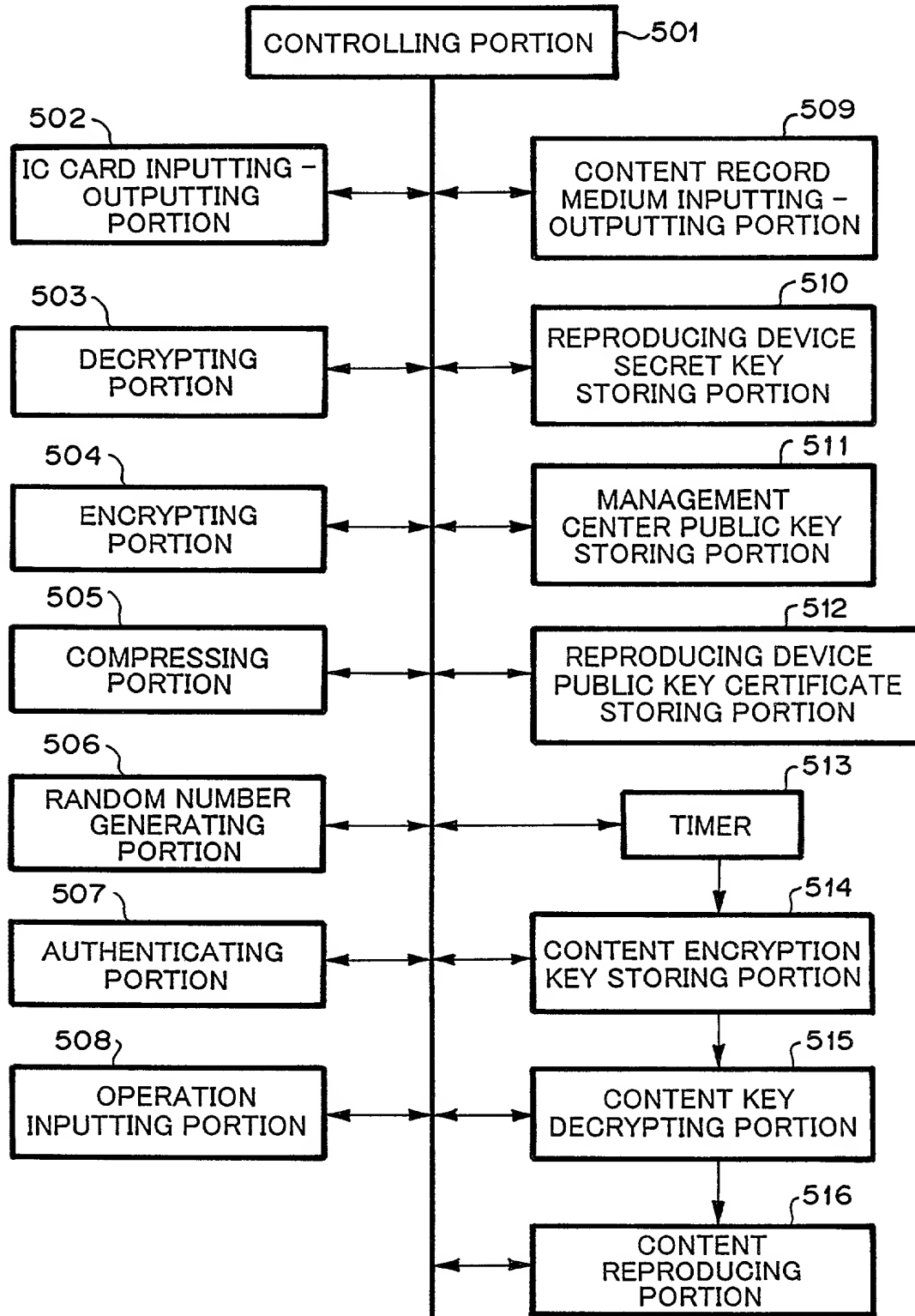
STRUCTURE OF SERVER 162

FIG.21



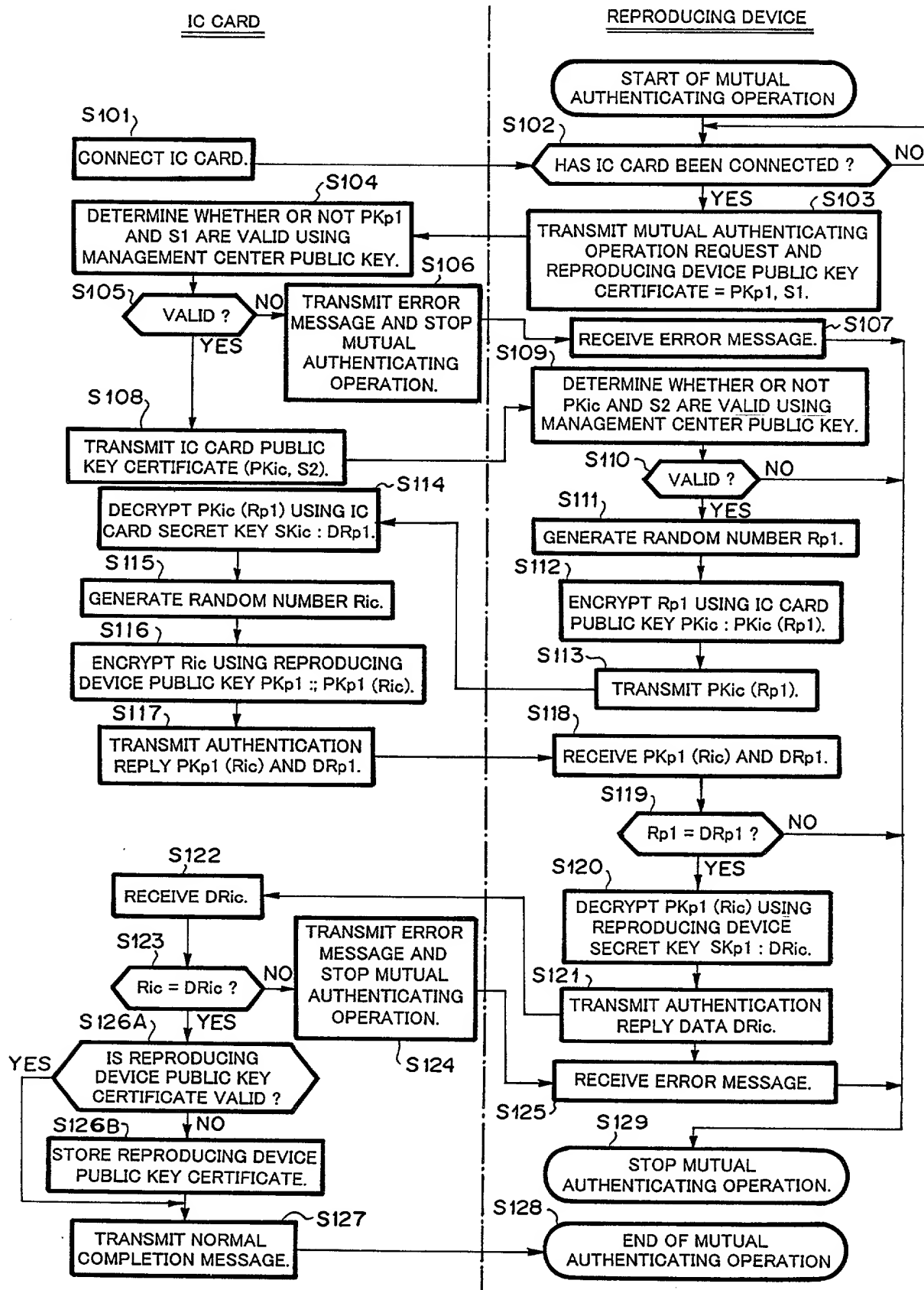
STRUCTURE OF IC CARD 167

FIG.22



STRUCTURE OF REPRODUCING DEVICE 170

FIG.23



# FIG.24

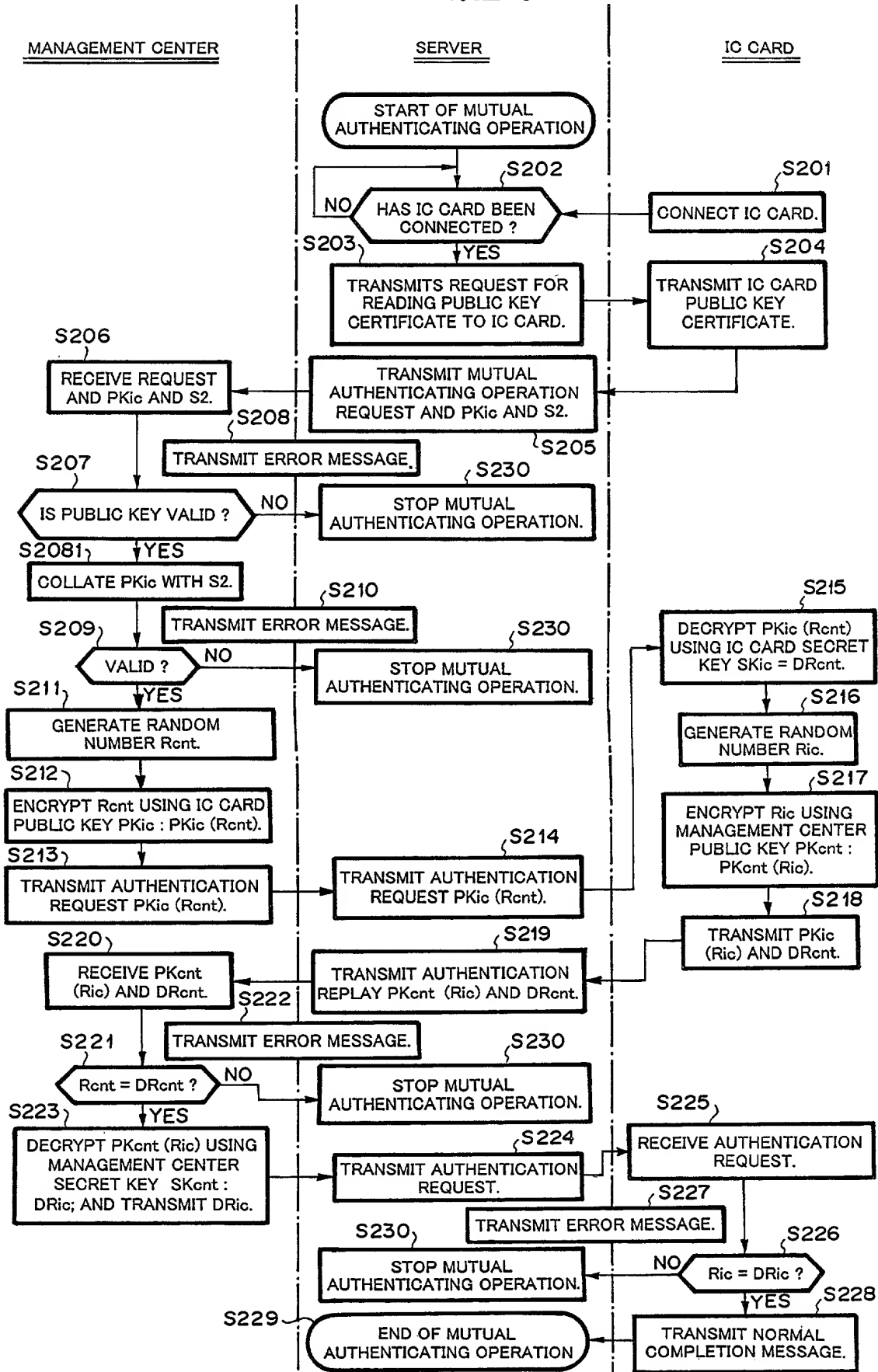
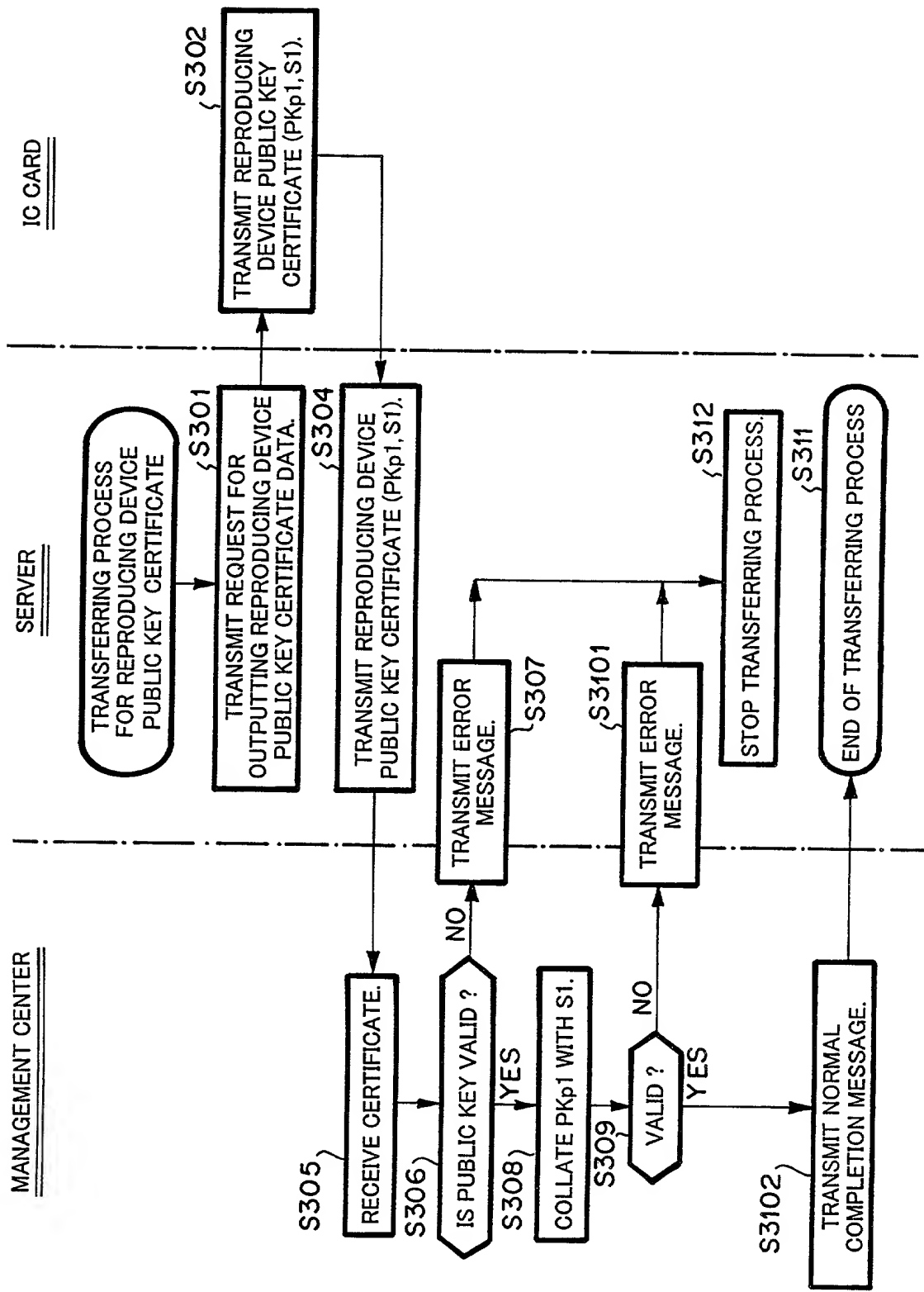


FIG. 25



# FIG.26

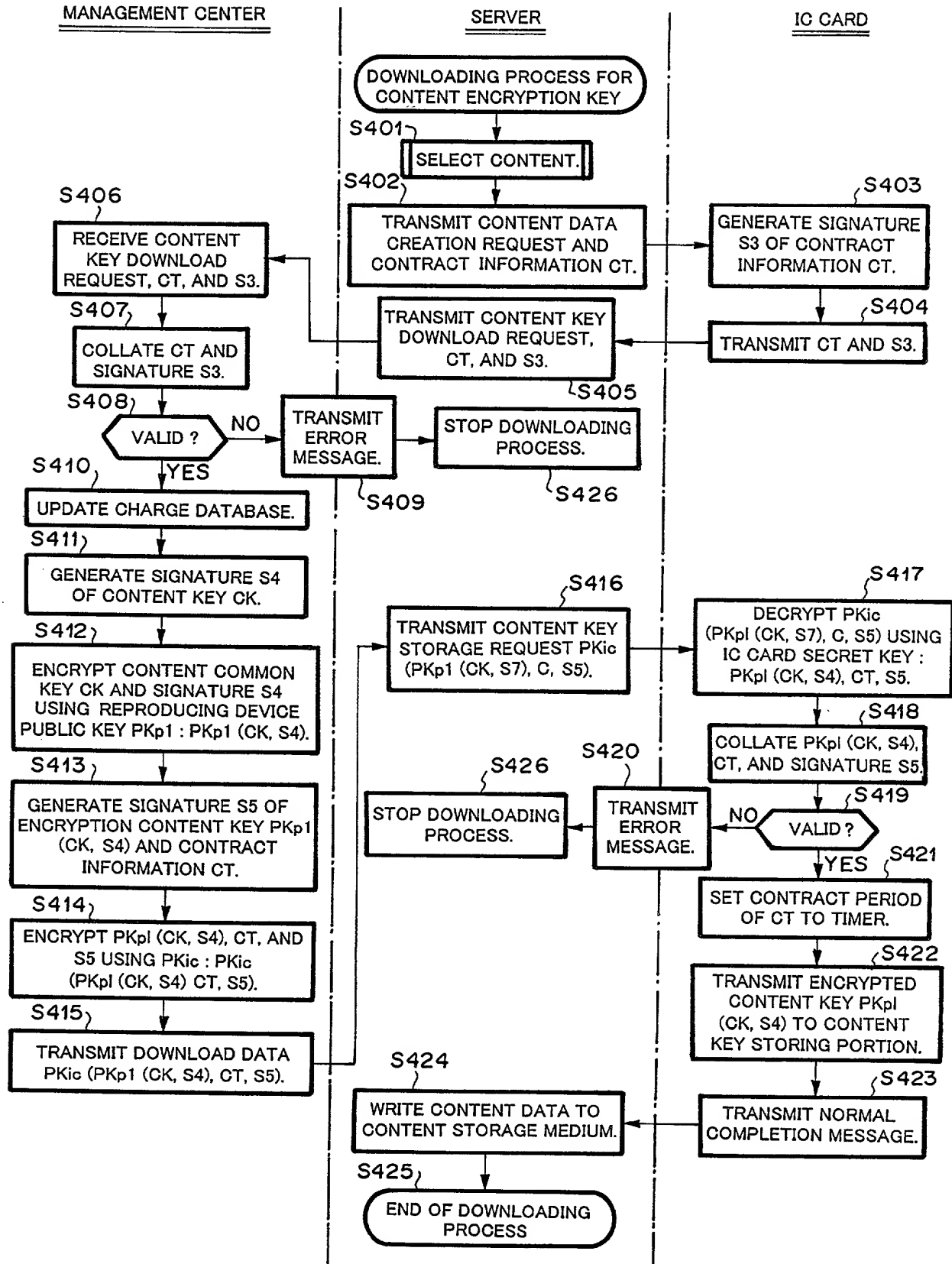


FIG.27

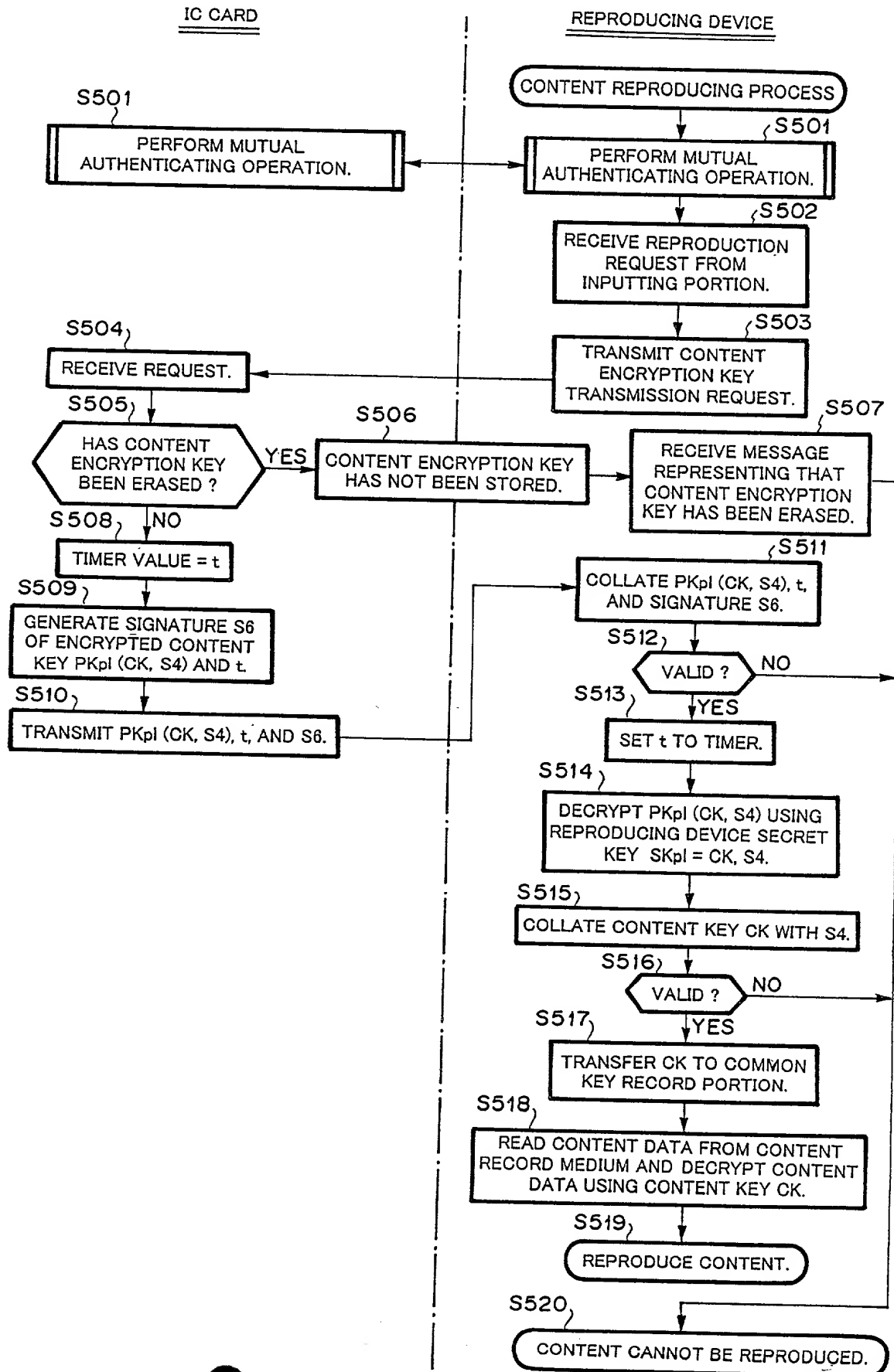




FIG.28

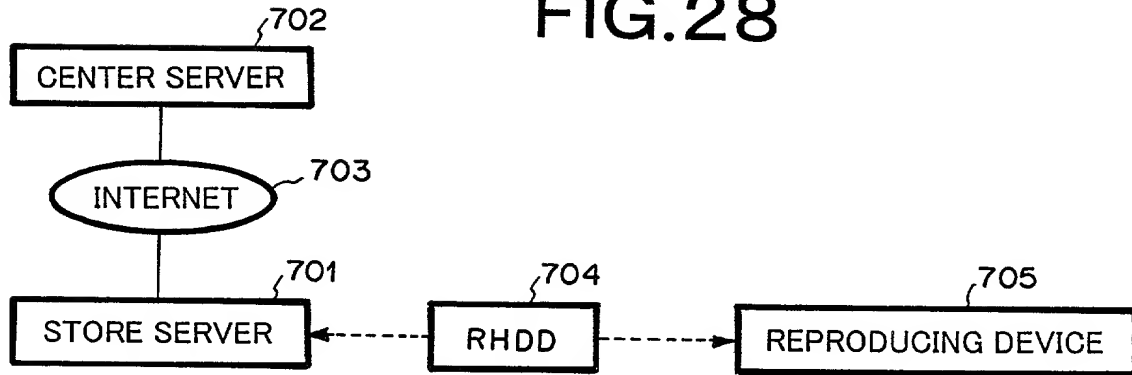


FIG.29

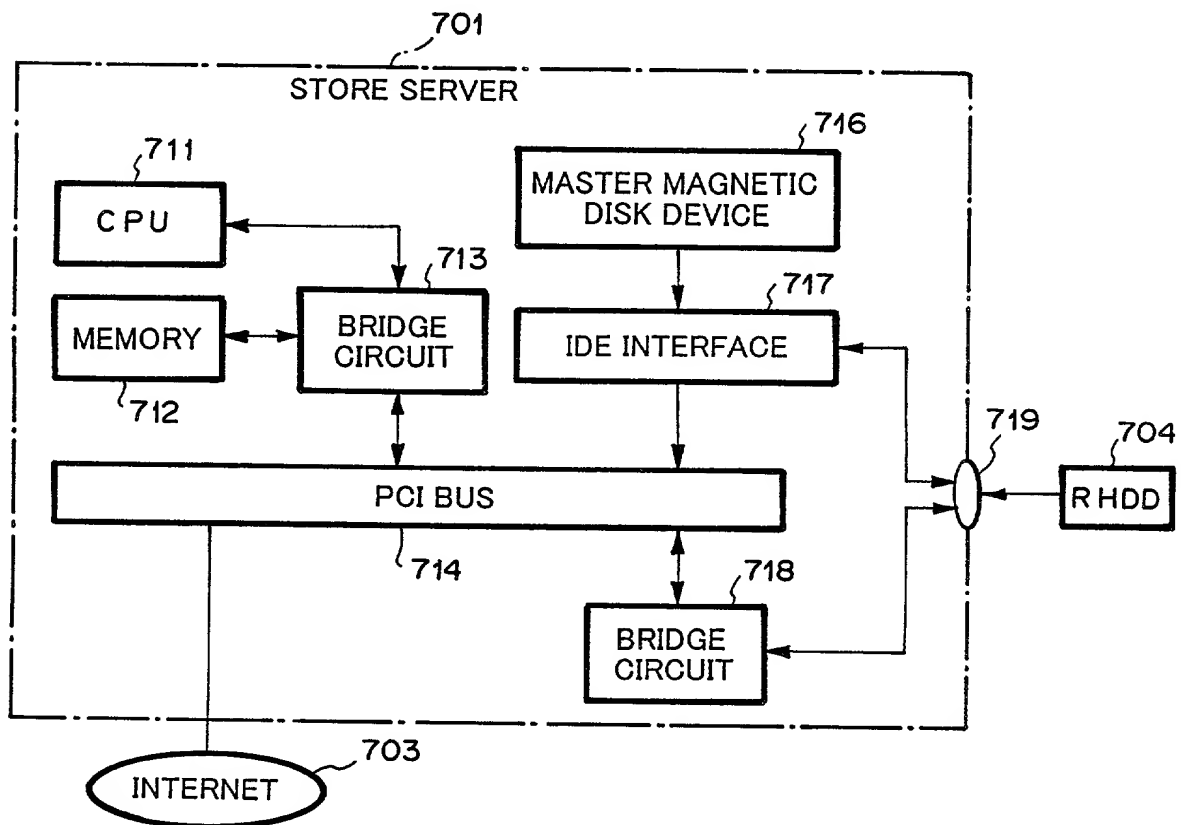


FIG.30

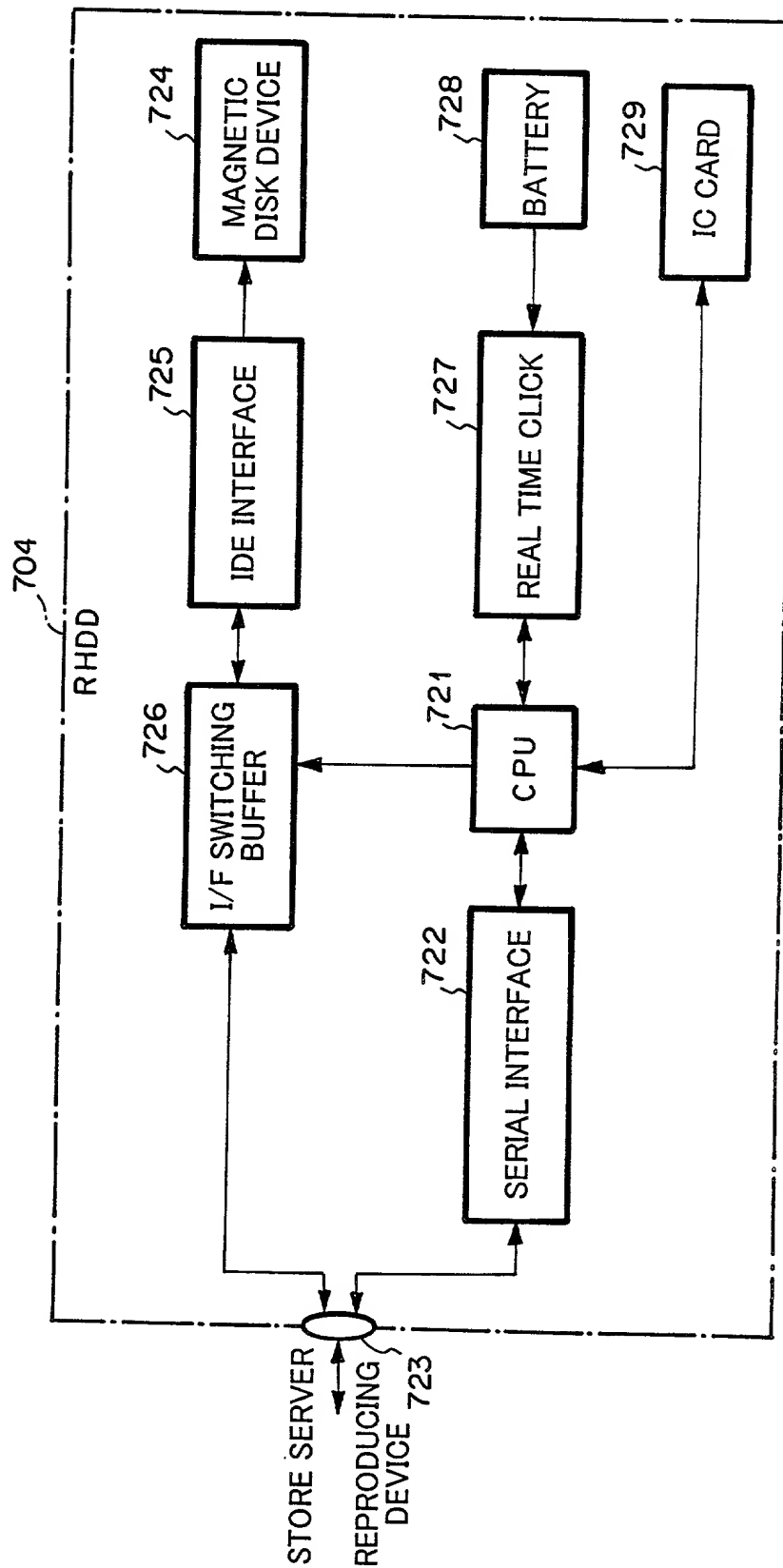


FIG. 31

